

**Form PTO-1449** (modified)

List of Patents and Publications  
For Applicant's Information  
Disclosure Statement  
(Use several sheets if necessary)

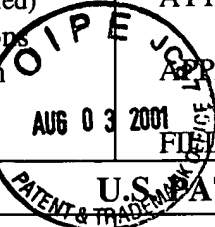
ATTY. DKT. NO. 5500-66100

APPLICANT: Christie, et al.

FILING DATE: April 2, 2001

SERIAL NO. 09/825,183

GROUP: 2183

**U.S. PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
B56	AA	5,438,668	8/1/95	Coon, et al.			
B50	AB	5,838,984	11/17/98	Nguyen, et al.			
B50	AC	5,848,284	12/8/98	Sharangpani			
B50	AD	5,274,834	12/28/93	Kardach, et al.			
B50	AE	5,125,087	6/23/92	Randell			
B50	AF	4,926,322	5/15/90	Stimac, et al.			
B50	AG	5,136,697	8/4/92	Johnson			
B50	AH	5053,631	10/1/91	Perlman, et al.			
B56	AI	5,129,067	7/7/92	Johnson			
B50	AJ	5,058,048	10/15/91	Gupta, et al.			
B50	AK	4,928,223	5/22/90	Dao, et al.			
B50	AL	4,044,338	8/23/77	Wolf			
B50	AM	4,453,212	6/5/84	Gaither, et al.			
B50	AN	4,807,115	2/21/89	Torng			
B50	AO	4,858,105	8/15/89	Kuriyama, et al.			
B50	AP	5,226,126	7/6/93	McFarland, et al.			
B50	AQ	5,226,130	7/6/93	Favor, et al.			
B50	AR	6,157,996	12/5/00	Christie, et al.			
B50	AS	4,385,352	5/24/83	Bienvenu			
B50	AT	4,972,338	11/20/90	Crawford, et al.			
B50	AU	5,109,334	4/28/92	Kamuro			
B50	AV	5,226,132	7/6/93	Yamamoto, et al.			
B50	AW	5,293,592	3/8/94	Fu, et al.			
B50	AX	5,321,836	6/14/94	Crawford, et al.			
B50	AY	5,375,213	12/20/94	Arai			
B50	AZ	5,689,672	11/18/97	Witt, et al.			
B50	BA	5,809,273	9/15/98	Favor, et al.			

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B56	BB	5,905,900	5/18/99	Combs, et al.			
B50	BC	5,481,684	1/2/96	Richter, et al.			
B50	BD	5,651,125	7/22/97	Witt, et al.			
B50	BE	6,014,739	1/11/00	Christie			
B50	BF	5,822,778	10/13/98	Dutton, et al.			
B56	BG	5,768,574	6/16/98	Dutton, et al.			
B50	BH	5,560,032	9/24/96	Nguyen, et al.			
B50	BI	4,050,094	9/20/77	Bourke			
B50	BJ	5,561,784	10/1/96	Chen, et al.			
B50	BK	5,471,593	11/28/95	Branigin			
B50	BL	4,835,734	5/30/89	Kodaira, et al.			
B50	BM	5,303,358	4/12/94	Baum			
B50	BN	4,109,311	8/22/78	Blum, et al.			
B50	CS	5,758,116	5/26/98	Lee, et al.			

**FOREIGN PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
B56	BO	259 095	3/9/88	EP			
B50	BP	381 471	8/8/90	EP			
B50	BQ	459 232	12/4/91	EP			
B50	BR	2263985	8/11/93	GB			
B50	BS	2263987	8/11/93	GB			
B50	BT	2281422	3/1/95	GB			
B50	BU	067 667	12/22/82	EP			
B50	BV	425 410	5/2/91	EP			
B50	BW	369 396	5/23/90	EP			
B56	BX	467 152	1/22/92	EP			
B50							

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<b>OTHER PART</b> (Including Author, Title, Date, Pertinent Pages, Etc.)			
B50	BY	<u>The Technology Behind Crusoe™ Processors, Low-Power x86-Compatible Processors Implemented with Code Morphing™ Software</u> , Transmeta Corporation, January 2000, pages 1-18.	
B50	CA	<u>Alpha Learns to Do Windows; Digital's FX!32 is The Key To Running Win32 Software on Alpha/Windows NT.</u> , Selinda Chiquoine, BYTE, August 4, 1998, 4 pages.	
B50	CB	<u>Awards Stack Up for DIGITAL FX!32 Windows Compatibility Software for ALPHA</u> , DIGITAL Press Releases, December 11, 1997, 7 pages.	
B50	CC	<u>DIGITAL FX!32; White Paper: How DIGITAL FX!32 Works</u> , DIGITAL Semiconductor, January 26, 1998, 4 pages.	
B50	CD	<u>An Alpha in PC Clothing; Digital Equipment's New x86 Emulator Technology Makes An Alpha System a Fast x86 Clone</u> , Tom Thompson, BYTE, August 4, 1998, 7 pages.	
B50	CE	<u>AMD 64-Bit Technology; The AMD x86 Architecture Programmers Overview</u> , AMD, Publication #24108 Rev: A, August 2000, pages 1-106.	
B50	CF	<u>AMD 64-Bit Technology; The AMD x86-64 Architecture Programmers Overview</u> , AMD, Publication #24108 Rev: C, January 2001, pages 1-128.	
B50	CG	<u>Intel486 DX Microprocessor</u> , Intel, 6 pages.	
B50	CH	Michael Slater, "AMD's K5 Designed to Outrun Pentium," Microprocessor Report, Volume 8, Number 14, October 24, 1994, 7 pages.	
B50	CFI	Sebastian Rupley and John Clyman, "P6: The Next Step?," PC Magazine, September 12, 1995, 16 pages.	
B50	CJ	Tom R. Halfhill, "AMD K6 Takes On Intel P6," BYTE, January 1996, 4 pages.	
B50	CK	<u>"Intel Architecture Software Developer's Manual, Volume 1: Basic Architecture"</u> , Intel Corporation, Prospect IL, 1996, 1997, Chapter 8: Programming With The Intel MMX™ Technology, pp. 8-1 through 8-15.	
B50	CL	Holstad, S., "Tutorial Tuesday: Decoding MMX" January 14, 1997, Earthlink Network, Inc. copyright 1997, 5 pages (see <a href="http://www.earthlink.net/daily/Tuesday/MMX">http://www.earthlink.net/daily/Tuesday/MMX</a> )	
B50	CM	<u>"Intel MMX™ Technology – Frequently Asked Questions"</u> 6 pages (see <a href="http://www.intel.com/drg/mmx/support/faq/htm">http://www.intel.com/drg/mmx/support/faq/htm</a> )	
B50	CN	Intel, "Pentium Processor Family User's Manual, Vol. 1: Pentium Processor Family Data Book," 1994, 2-1 to 2-4.	
B50	CO	Kathail et al., HPL Playdoh Architecture Specification: Version 1.0, Hewlett Packard, Computer Systems Laboratory, HPL-93-80, February, 1994, pp. 1-48.	
B50	CP	IEEE MICRO, vol. 13, no. 5, October 1, 1993, pp. 24-36, Makato Awaga et al, "The VP 64-Bit Vector Coprocessor: A New Implementation of High-Performance Numerical Computation."	
B50	CQ	Intel Microprocessors: Vol. II, 1993, pp. 2-2, 2-19 to 2-26, 2-80 to 2-83 and 2-122.	
B50	CR	Intel Microprocessors: Vol. I, 1993, pp. 2-1, 2-18 to 2-26, 2-79 to 2-83 and 2-121 to 2-122.	

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Information Disclosure Statement--PTO 1449 (modified)

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